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Special Topic: Novel Optoelectronic Devices

Recent progress in graphene-based optical modulators on silicon photonics platform

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Abstract: Graphene is a 2D material which has attracted tremendous interest from academia and industry, due to its attractive electrical and optical properties. By integrating graphene onto silicon photonics platform, various high-performance electro-optic and thermo-optic modulators have been demonstrated. In this paper, such graphene-based optical modulators are reviewed. The concept and principle of optical modulations are firstly analyzed and then an overview of the developing trends of optical modulators is presented. Meanwhile, the performances of graphene-based optical modulators, including power consumptions and speeds of electro-optic modulators as well as tuning efficiencies of thermo-optic modulators, are then evaluated and discussed in detail. All these optical modulators would play important roles in a wide range of applications including telecom, interconnects, computing, quantum information processing, and beam steering.

Keywords: optical modulator, graphene, integrated, high-speed

Introduction

Optical modulators are key components in the optical communication and optical computing systems [1–3]. By employing optical modulators, electrical signals can be converted to optical signals, which can thus be used for telecom, optical signal processing, optical computing, etc. To increase the capacities of optical systems and implement complex functions, large-scale integrated photonic circuits with high levels of integration are needed, in which high-performance integrated optical modulators are required. To obtain high-performance photonic circuits, integrated optical modulators with high speeds, low power consumptions, and high modulation efficiencies are highly desired.

Silicon photonics is considered an ideal platform for on-chip integration, owing to the advantages of compact footprint, low power consumption, and compatibility with complementary metal-oxide-semiconductor (CMOS) processes [4–10]. With rapid developments of silicon photonics, various silicon/silicon nitride-based integrated devices on the silicon photonics platform have been demonstrated [11–13]. Silicon nitride-based integrated devices on the silicon photonic platform are usually built with a silica buffer layer on a silicon wafer. By using carrier-plasma dispersion effect of silicon, high-speed electro-optic (EO) mod-

ulators can be realized [14–16]. Moreover, high tuning efficiency thermo-optic (TO) modulators can also be fabricated by using the TO effect of silicon or silicon nitride [17–20]. However, based on silicon photonic technology, it is still a challenge to build high-performance integrated EO/TO modulators with ultra-low power consumptions, ultra-high speeds, and high modulation efficiencies.

Graphene, a single sheet of carbon atoms in a hexagonal lattice, has attracted tremendous interest due to its attractive electrical and optical properties [21–30]. Graphene has an ultra-high carrier mobility of $\sim 200,000 \text{ cm}^2 \text{ v}^{-1} \text{ s}^{-1}$ at room temperature [31] and ultra-broadband optical absorption [32]. Moreover, the Fermi level of graphene (E_f) can be tuned through electric gating. Thus, by integrating graphene onto silicon photonics platform, ultra-high speed EO modulators could be built [33–35]. Meanwhile, monolayer graphene also has a low optical absorption of 2.3% for normal incident light [36]. Then, it can directly contact integrated waveguides. Furthermore, graphene has a high thermal conductivity of up to $2000 \text{ W m}^{-1} \text{ K}^{-1}$. Thus, graphene-based micro-heaters can be used to build high-efficiency integrated TO modulators on silicon photonics platform [37–39].

In this review, we present a review of the current developments of near-infrared (NIR) integrated graphene-based optical modulators on silicon photonics platform and also indicate their developing trends in the near future [26,34,35,37–66]. This review is organized as follows. The Materials section focuses on the material properties. Next, the working principles of graphene-based optical modulators are analyzed. Then, integrated graphene-based EO/TO modulators are discussed. Conclusion and perspective are presented in the last section.

Materials

Silicon (Si), silicon nitride (Si₃N₄) and silica (SiO₂)

To realize integrated waveguides and then build optical devices, various materials have been used on silicon photonics platform. The bandgap of silicon, silicon nitride, and silica is 1.1, 5.3 and 9 eV, respectively, which are larger than the photon energy in the near infrared range ($E_p=0.86 \text{ eV}$ at 1550 nm). Then, they would have low optical absorption losses in the communication band. Moreover, all these three materials are compatible with the CMOS fabrication process and capable for large-scale device integration. Thus, silicon, silicon nitride, and silica are always used as basic materials for integrated photonic circuits. Their material properties are provided in Table 1 [67–75].

In the photonic circuit on silicon photonics platform, integrated waveguides are usually introduced based on silicon and silica. Silicon has a high refractive index of ~ 3.467 , and silica has a low refractive index of 1.444. Thus, by using silicon as the waveguide core material and silica as the waveguide cladding material, the optical power can be confined in the waveguide core and a low propagation loss of $\sim 4 \text{ dB cm}^{-1}$ could be obtained [67]. Meanwhile, to obtain electro-optic tuning, the refractive index of silicon can be tuned through carrier-plasma dispersion effect [76]. Moreover, silicon also has a strong thermo-optic effect with a TO tuning efficiency of $1.86 \times 10^{-4} \text{ K}^{-1}$ [68]. Thus, one can build active optical modulators based on the EO and TO effects of silicon. However, the carrier mobility of silicon is only $1350 \text{ cm}^2 \text{ v}^{-1} \text{ s}^{-1}$ [69] and the thermal conductivity of silicon is $80 \text{ W m}^{-1} \text{ K}^{-1}$, which would limit their device performances in optical modulations.

To reduce the optical transmission loss, one can use silicon nitride as the waveguide core material and use

Table 1 Electrical and optical properties of silicon, silicon nitride, and silica

Material	Refractive index	Bandgap (eV)	TO coefficient (K^{-1})	Thermal conductivity ($W m^{-1} K^{-1}$)	Carrier mobility ($cm^2 v^{-1} s^{-1}$)	Propagation loss ($dB cm^{-1}$) ^{a)}
Silicon [67–69]	3.467	1.1	1.86×10^{-4}	80	1350	4
Silicon nitride [70–73]	~2.0	5.3	2.5×10^{-5}	20	–	0.06
Silica [71,74,75]	1.444	9	0.95×10^{-5}	1.38	–	0.02 ^{b)}

a) Propagation loss of integrated waveguides. In such waveguides, the corresponding material in the table is used as the waveguide core material and silica is used as the cladding material. b) Extra doping process is required for the silica waveguide core. Thus, silica waveguide is usually not utilized on silicon photonics platform.

silica as the waveguide cladding material. Since silicon nitride has a low refractive index, the size of the silicon nitride waveguide would be relatively large. Meanwhile, the power confinement of silicon nitride waveguide would be a little lower than that of silicon waveguide. With a waveguide dimension of $2.8 \mu m$ and $100 nm$, a low propagation loss of $\sim 0.06 dB cm^{-1}$ has been obtained for a silicon nitride waveguide [70]. However, silicon nitride is an insulator material and does not have free carriers. Thus, it could not be directly used in the electro-optic modulation. Meanwhile, the thermo-optic effect and the thermal conductivity of silicon nitride are relatively low [71], as shown in Table 1. Thus, to implement optical modulators on Si_3N_4 waveguides, new materials should be introduced, especially for high-speed EO modulators.

Graphene

Graphene is a sheet of carbon atoms in a hexagonal lattice with photon-like massless and gapless electrons, as shown in Figure 1A [24,77]. Owing to the gapless linear dispersion of Dirac electrons in graphene, it has a wide-band operating spectral range [78]. Meanwhile, the Fermi level of graphene can be tuned through electrical gating [79,80]. Moreover, as shown in Figure 1B, the interband absorption can be blocked from Pauli blocking when $2E_f > E_p$ is satisfied. Here, $E_p = \hbar\omega$ is the photon energy, which is $0.86 eV$ at $1550 nm$. Thus, in the communication band, the optical absorption can be suppressed if E_f is larger than $0.43 eV$. Meanwhile, the carrier mobility and thermal conductivity of graphene are $\sim 200,000 cm^2 v^{-1} s^{-1}$ and $\sim 2000 W m^{-1} K^{-1}$, respectively. With such a high carrier mobility and a high thermal conductivity, graphene

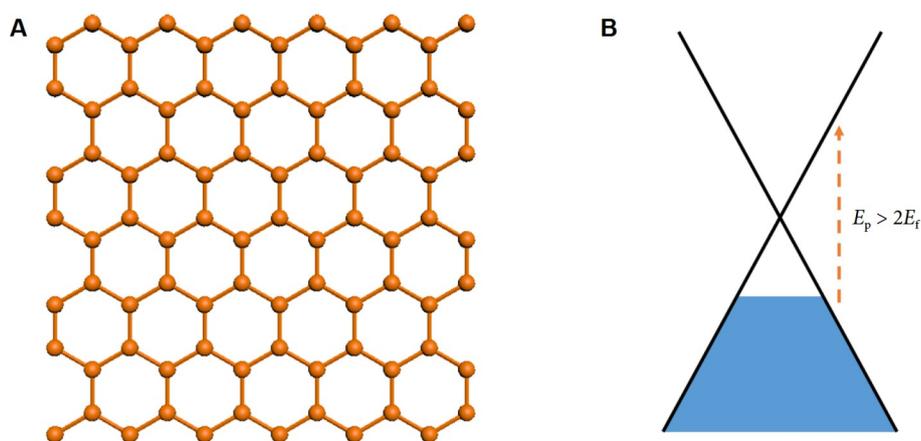


Figure 1 (A) Two-dimensional honeycomb structure of carbon atoms in graphene; (B) schematic diagram of possible interband optical transitions in graphene.

can thus be used in high-speed EO and TO modulations. However, since the thickness of graphene is only ~ 0.5 nm, the light-graphene interaction is low. Thus, to build integrated optoelectronic devices, graphene should always be integrated on optical waveguides to enhance the light-graphene interaction.

Principle of graphene-based optical modulators

Hybrid waveguide structure and optical properties

Based on the aforementioned material properties of silicon and silicon nitride on silicon photonics platform, one can find that new materials should be introduced to improve device performances of silicon and silicon nitride optical modulators. Meanwhile, graphene has promising properties to solve this issue. Thus, high-performance integrated optical modulators based on graphene-silicon and graphene-Si₃N₄ hybrid waveguides have been constructed.

Before discussing principles of the EO and TO modulations, we first analyze the electrical field distribution of a typical graphene-silicon hybrid waveguide as shown in Figure 2A. The dimension of the silicon waveguide is 500 nm \times 220 nm. The thickness of the Al₂O₃ and graphene is set to 7 and 0.5 nm, respectively. Meanwhile, the thickness of buried oxide (BOX) is 2 μ m while the top cladding is air. Here, the permittivity of graphene is set to be $3.460+5.95i$ with a fermi level of 0.4 eV [33,81]. Then, as shown in Figure 2B, most of the optical power is confined in the silicon core and $\sim 0.07\%$ light power is evanescently coupled to the graphene layer. In such hybrid graphene-silicon waveguide, the propagation loss is ~ 200 dB cm⁻¹ for the TE mode polarization [35]. The loss of such hybrid waveguide is polarization-dependent, which can be used for polarizers [82–84]. Here we use a typical graphene-silicon hybrid waveguide to analyze the electrical field distribution while the structure parameters of the waveguide can be modified for different applications in optical modulations.

Moreover, the propagation loss of the graphene-silicon waveguide can be decreased by lowering the optical power located in the graphene layer. This can be realized through enlarging the dimension of the waveguide or introducing a thick spacer material (i.e., Al₂O₃) between the graphene and silicon [37]. With such configurations, the light power in the graphene layer can be decreased. Then, the power ratio between the optical power in the graphene layer and the total optical power in the waveguide would be reduced. The imaginary part of the effective index of the graphene-silicon waveguide can be decreased, which depends on

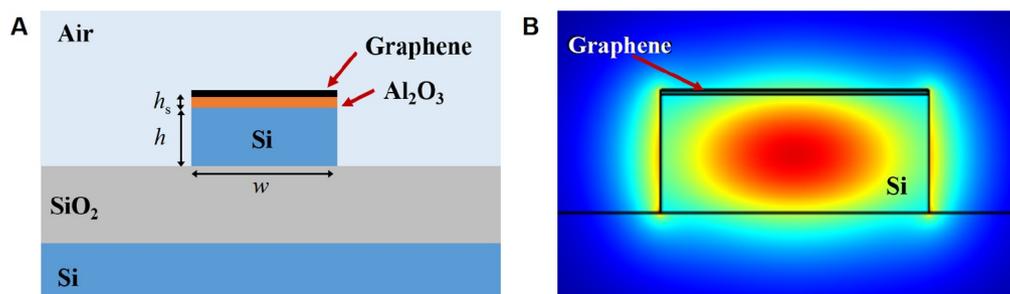


Figure 2 (A) Crosssection of a graphene-silicon hybrid waveguide; (B) electric field distribution profile of the TE fundamental mode. The waveguide width (w) is 500 nm and the waveguide height (h) is 220 nm. Al₂O₃ is the spacer material with a height (h_s) of 7 nm.

the energy ratio [85]. Since the imaginary part of the effective index determines the propagation loss, the loss induced by graphene could then be suppressed [86,87]. In this way, the quality factor for the graphene-silicon based resonator could be increased. However, the modulation efficiency would also be decreased. Thus, there is a tradeoff between the waveguide loss and the modulation efficiency for optical modulators based on such a hybrid waveguide configuration.

Furthermore, beside the structure of the graphene-silicon hybrid waveguide shown in Figure 2A, the silicon waveguide core could also be covered by silica, which is used to obtain a flat surface [48]. In that structure, silica is deposited by plasma enhanced chemical vapor deposition (PECVD) after the silicon waveguide fabrication. Then, silica cladding is planarized to the top surface of the waveguide based on standard chemical mechanical planarization (CMP) techniques. Thus, a flat surface can be provided before graphene transfer, which can protect graphene in the fabrication process. It should be noted that such a structure would only slightly affect the electrical field distribution and the modulation efficiency.

Based on such aforementioned hybrid waveguide structures, optical modulators can be built. In those modulators, optical modulations can be obtained by changing the effective index of the waveguides. In the EO modulation, the effective index of the waveguide can be tuned through changing the fermi level of graphene. In the TO modulation, with an applied heating power in the graphene layer, the silicon core will be heated and then the effective index of the waveguide can be changed. Here, the intrinsic speed of the EO modulation is >1 GHz, which is much faster than that of the TO modulation ~KHz. Detailed principles of EO and TO modulations will be presented in the next two sub-sections.

Electro-optic modulation

In the near-infrared region, based on the random phase approximation and the Kramers-Kronig relation, the gate-dependent permittivity of graphene has been extensively studied, which can be expressed as follows [33,81]:

$$\varepsilon_g'(E_p) = 1 + \frac{e^2}{8\pi E_p \varepsilon_0 d} \ln \frac{(E_p + 2|E_f|)^2 + \Gamma^2}{(E_p - 2|E_f|)^2 + \Gamma^2} - \frac{e^2}{\pi \varepsilon_0 d} \frac{|E_f|}{E_p^2 + (1/\tau)^2}, \quad (1)$$

$$\varepsilon_g''(E_p) = \frac{e^2}{4E_p \varepsilon_0 d} \left[1 + \frac{1}{\pi} \left(\tan^{-1} \frac{E_p - 2|E_f|}{\Gamma} - \tan^{-1} \frac{E_p + 2|E_f|}{\Gamma} \right) \right] + \frac{e^2}{\pi \tau E_p \varepsilon_0 d} \frac{|E_f|}{E_p^2 + (1/\tau)^2}, \quad (2)$$

where ε_g' and ε_g'' are the real part and imaginary part of the permittivity, respectively. Meanwhile, d is the thickness of graphene, which is set to 0.5 nm. Γ is the interband linewidth broadening, which is set to 110 meV. In the equation, the free carrier scattering rate $1/\tau$ can be neglected since it is much smaller than the incident photon energy. Figure 3A shows the real part and imaginary part of the permittivity under different Fermi levels. Here, the light wavelength is set to 1550 nm. One can find that the real part reaches its maximum when the Fermi level is 0.4 eV. Meanwhile, when the Fermi level increased to be larger than 0.49 eV, the real part would be less than 0. This indicates that graphene has metallic properties. Moreover, when the Fermi level increases from 0 to 1 eV, the interband absorption is suppressed due to the Pauli blocking. Then, the imaginary part of graphene decreases if the Fermi level increases.

Based on the gate dependent permittivity of graphene, the effective index of the hybrid waveguide shown in Figure 2A can be calculated. As shown in Figure 3B, the effective modal index of the hybrid waveguide is

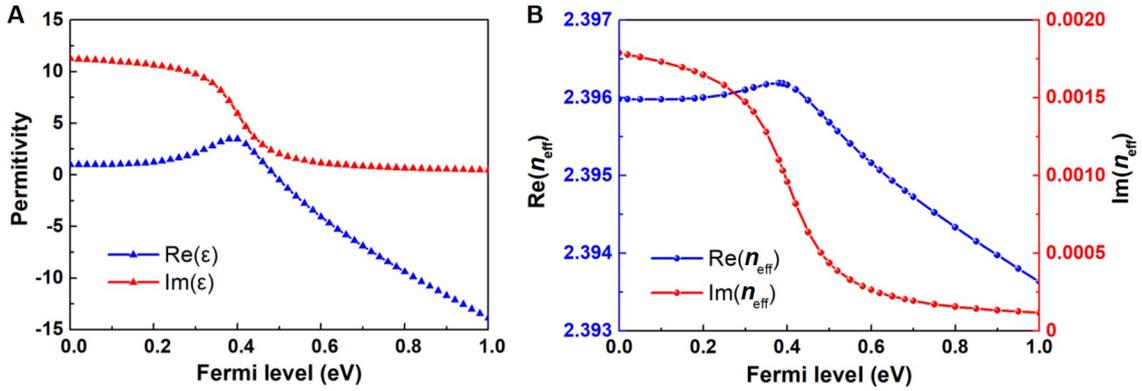


Figure 3 (A) Real part and imaginary part of the anisotropic in-plane permittivity of graphene under different Fermi levels. (B) Real and imaginary parts of effective modal index (n_{eff}) of a 500 nm×220 nm single-mode silicon waveguide as a function of the Fermi level.

changed with respect to the Fermi level. The real part of the effective index determines the phase change in the waveguide while the imaginary part determines the loss in the hybrid waveguide. Thus, by changing the Fermi level of graphene, the phase and the loss in the waveguide can both be changed, simultaneously. In this way, the EO modulation can be obtained. Moreover, a high modulation speed could be expected since graphene has a large carrier mobility of $\sim 200,000 \text{ cm}^2 \text{ v}^{-1} \text{ s}^{-1}$.

Here we use monolayer graphene to illustrate the principle of the EO modulation for the graphene-based modulators on silicon photonics platform. In such devices, graphene-silicon capacitors are formed. The Fermi level of graphene is usually tuned by applying gate voltage between the graphene and doped silicon [40]. Moreover, double-layer graphene modulators can also be implemented, in which a graphene-graphene capacitor can be built and the gate voltage is applied between two graphene layers. Then, the Fermi levels of the two graphene layers would be tuned in different directions. In this way, the permittivity of two-layer graphene can be changed and the EO modulation can also be obtained [43].

Thermo-optic modulation

In this sub-section, the concept for the graphene-based TO modulation on silicon photonics platform is evaluated based on the hybrid waveguide shown in Figure 2A. Here the spacer material (Al_2O_3) is not used. For a graphene-based TO modulator, the electrical power can be applied onto the graphene layer. The graphene layer can absorb the electrical power and convert it to heat. Then, the graphene layer can work as a micro-heater and heat the integrated waveguide underneath. For such a graphene-silicon hybrid structure, the 2D temperature distribution is shown in Figure 4. In the simulation, the thermal conductivities of silicon, silicon oxide, and graphene are set to ~ 80 , ~ 1.38 , and $\sim 2000 \text{ W m}^{-1} \text{ K}^{-1}$, respectively. The heat convection coefficient of air is set to $5 \text{ W m}^{-2} \text{ K}^{-1}$. When the thermal energy density (Q) in the graphene layer is set to $6 \times 10^{16} \text{ W m}^{-3}$, the temperature of the silicon waveguide increases by $\sim 10 \text{ K}$. Since the TO coefficient of silicon is $dn/dT = 1.86 \times 10^{-4} \text{ K}^{-1}$, the refractive index of silicon can then increase. Thus, the effective index of the graphene-silicon waveguide (n_{eff}) can be tuned. The real part of n_{eff} determines the waveguide phase delay and the imaginary part affects the loss. Thus, the TO modulation based on a graphene heater can then be obtained.

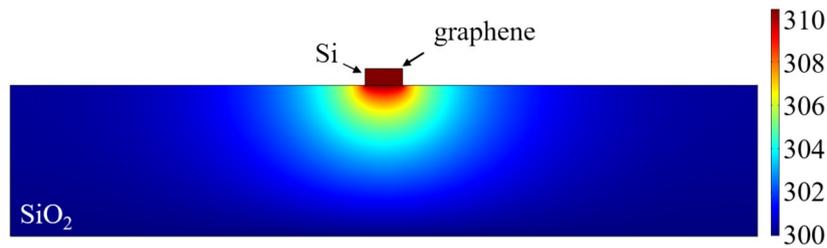


Figure 4 2D temperature distribution of a hybrid graphene-silicon waveguide. The silicon waveguide has a dimension of 500 nm×220 nm. The thickness of graphene is 0.5 nm. Moreover, the spacer material is not used here and h_s is set to 0.

State-of-the-art of graphene-based optical modulators

This section reviews recent developments of graphene-based EO/TO modulators on silicon photonics platform, by using principles discussed above. For the EO modulation, the chip-integrated EO modulators and free-space optical modulators will be presented. The modulation depths, modulation speeds, and power consumption will be evaluated. For the TO modulation, waveguide-coupled optical modulators will be discussed, in which the TO tuning efficiencies and thermal responses would be elaborated. All those integrated devices work in the communication band.

Chip-integrated EO modulators

Chip-integrated EO modulators are key components for the on-chip optical interconnects. To obtain high-performance photonic circuits, modulation speeds and power consumption are key parameters for such devices. In the following sub-sections, various graphene-based chip-integrated modulators are presented. All those modulators follow the principle of the EO modulation discussed above.

Waveguide-coupled optical modulators

For waveguide-coupled optical modulators, monolayer graphene-based optical modulators have attracted much attention from researchers. As shown in Figures 5A–5C, Liu *et al.* [40] demonstrated the first electro-absorption optical modulator in 2011. In that device, the chemical vapour deposition (CVD) grown graphene is used and mechanically transferred onto the silicon waveguide. The length of the graphene-silicon hybrid waveguide is set to 40 μm and the thickness of Al_2O_3 is 7 nm. By applying gate voltage between the graphene and doped silicon, the Fermi level of graphene can be tuned. Then, the optical modulation can be obtained with a modulation efficiency of $\sim 0.1 \text{ dB } \mu\text{m}^{-1}$ while the modulation bandwidth is 1.2 GHz. To meet the requirement of optical communication systems, the performances of monolayer graphene-based optical modulators are further improved [34,41,42]. In 2016, Hu *et al.* [41] built a high-speed electro-absorption modulator, as shown in Figures 5D–5F. In that device, the insertion loss is 3.8 dB and the modulation depth is $\sim 5.2 \text{ dB}$, by optimizing the device design and fabrication process. Meanwhile, the series resistor is reduced to 241 Ω and the capacitor is about 80 fF. Then, a modulation speed of 10 Gb s^{-1} is obtained with a 3-dB bandwidth of 5.8 GHz. Moreover, the power consumption is about 350 fJ bit^{-1} .

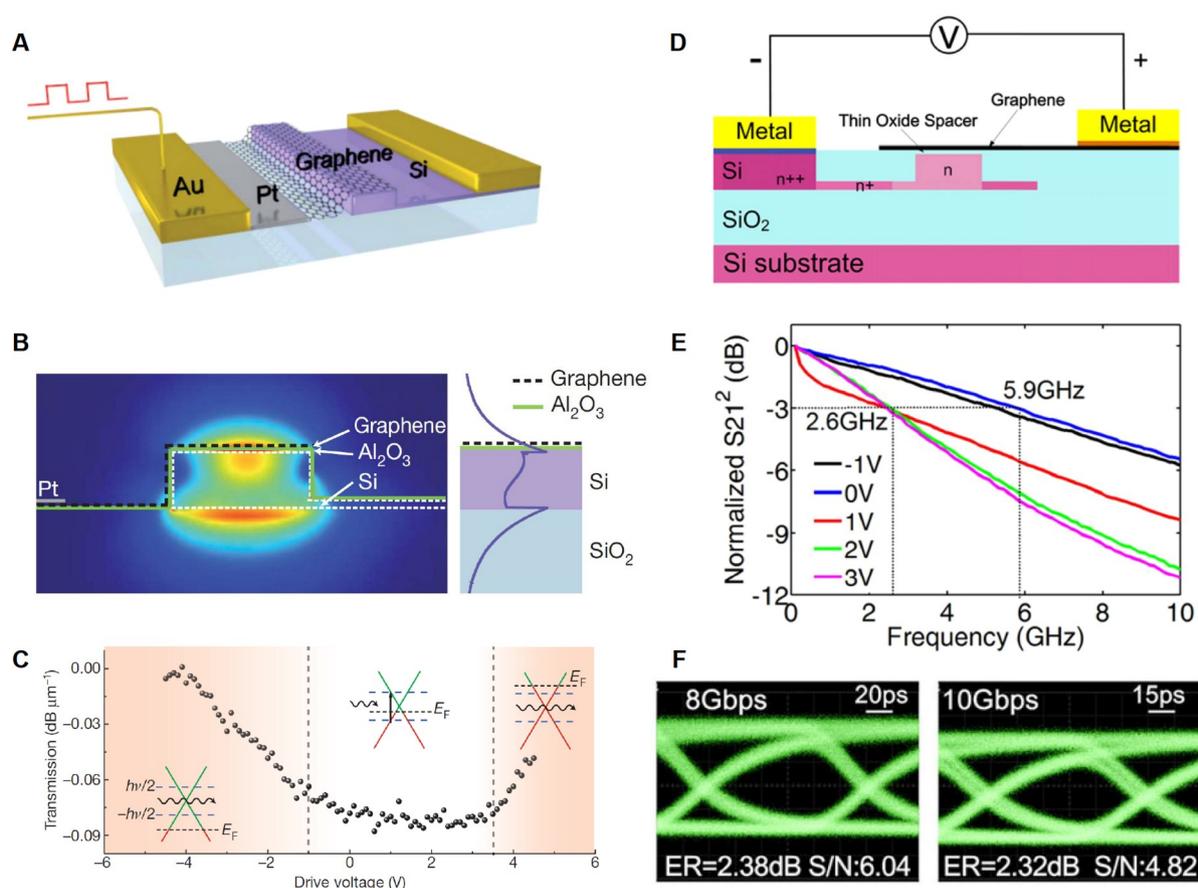


Figure 5 Chip-integrated monolayer graphene EO modulators. (A) Schematic illustration of a graphene-based optical modulator; (B) electrical field distribution of the hybrid graphene-silicon waveguide; (C) static EO response of the device [40]; (D) diagram of a high-speed EO modulator with a speed of 10 Gb s^{-1} ; (E) measured and normalized EO S_{21} responses; (F) optical eye diagrams at 8 and 10 Gb s^{-1} modulation speeds [41]. (A)–(C) Adapted with permission from [40]. Copyright 2011, Springer Nature. (D)–(F) Adapted with permission from [41]. Copyright 2016, WILEY-VCH.

To increase the modulation efficiency, double-layer graphene electro-absorption modulators are implemented. As shown in Figures 6A–6C, a double-layer graphene-based optical modulator was demonstrated in 2012 [43]. In that device, the capacitor is formed by a graphene-SiN/hBN-graphene structure. The Fermi levels of both two graphene layers can be tuned, as shown in Figure 6C. The modulation efficiency of the device is about $0.16 \text{ dB } \mu\text{m}^{-1}$ which is twice as high as that of the monolayer graphene-based optical modulator [40]. Moreover, the 3-dB bandwidth is about 1 GHz, which is limited by the high series resistance of $\sim 1 \text{ k}\Omega$ in the device fabrication process. Then, to improve the modulation speed, several approaches have been proposed and demonstrated [44–47]. In 2019, Giambra *et al.* [47] demonstrated a high-speed double-layer graphene-based optical modulator, as shown in Figures 6D–6F. The device length is optimized to be $120 \mu\text{m}$ and the modulation efficiency is about $0.137 \text{ dB } \mu\text{m}^{-1}$. To reduce the series resistance, top contacts are made of a stack of Nickel (Ni) and Gold (Au). Then, the contact resistance is reduced to about $<10 \Omega$. Meanwhile, the electrical capacitor is 204 fF. Thus, the 3-dB bandwidth is about 29 GHz. Furthermore, a modulation speed of 50 Gb s^{-1} is obtained with an extinction ratio (ER) of 1.3 dB, as shown in the eye diagram.

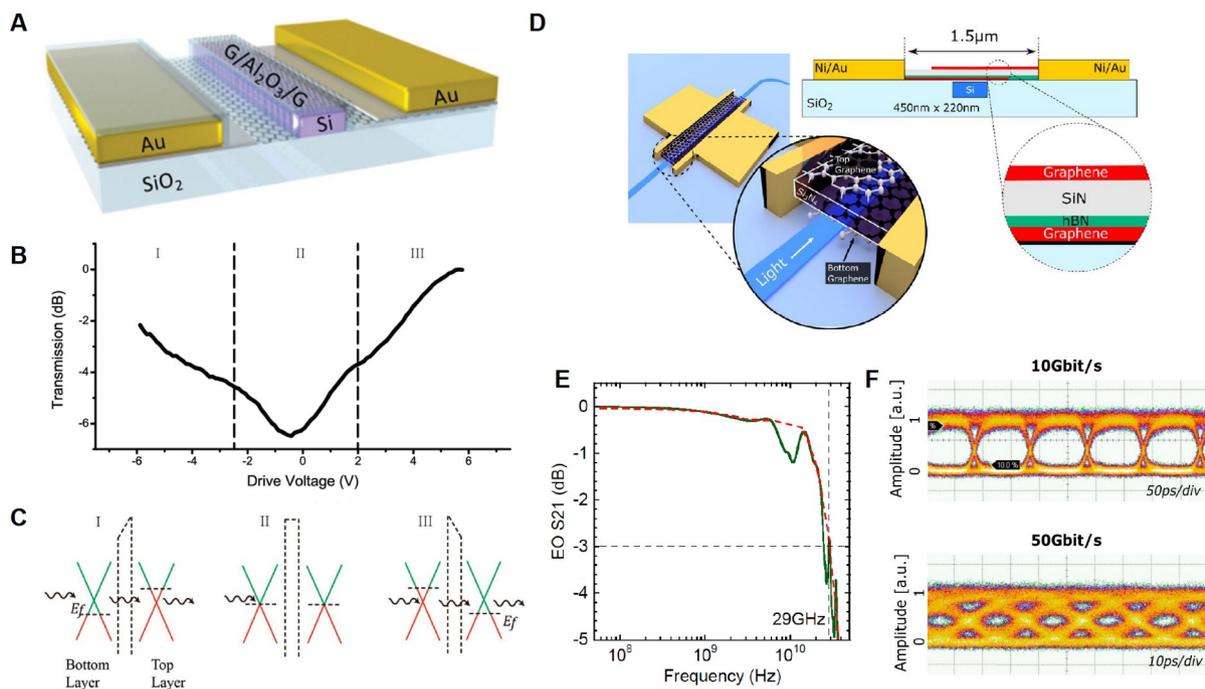


Figure 6 Chip-integrated double-layer graphene EO modulators. (A) Schematic illustration of a double-layer graphene modulator; (B) static response of the modulator; (C) the graphene band profiles (green for unoccupied state, red for occupied states) for regions I, II, and III in (B) [43]; (D) diagram of a high-speed double-layer graphene modulator; (E) EO S_{21} bandwidth of the double-layer graphene optical modulator; (F) eye diagrams at data rates of 10 and 50 Gb s^{-1} [47]. (A–C) Adapted with permission from [43]. Copyright 2012, American Chemical Society. (D–F) Adapted with permission from [47]. Copyright 2019, Optica publishing group.

Waveguide-coupled phase modulators

Graphene can also be used to build the optical phase modulators. As shown in Figure 3B, the imaginary part of the effective index is relatively small, when $E_F > 0.4$ eV. This indicates that the loss of the hybrid waveguide is low. In this condition, the real part of the effective index of the hybrid graphene-silicon waveguide can be effectively tuned. Then, a strong phase modulation can be obtained.

A graphene-silicon high-speed phase modulator was first demonstrated in 2018 [48]. In that device, monolayer graphene is integrated onto both two arms of the Mach-Zehnder interferometers (MZI) structure, as shown in Figure 7. The length of the phase-shifter is 300 μm and the extinction of the modulator is about 35 dB. Meanwhile, graphene is gated beyond the Pauli blocking condition ($E_F > 0.4$ eV at 1.55 μm), and the device then operates in the low-loss region. Thus, the phase modulation is dominant with respect to amplitude changes. The modulation efficiency of the phase shifter is measured to be about 0.28 V cm at 1550 nm. Moreover, with $2 \cdot V_{pp}$ driving voltage in a push-pull configuration, the device can operate at a speed of 10 Gb s^{-1} with an EO bandwidth of 5 GHz. The fabrication process of the graphene-based phase modulators is similar to those of the abovementioned graphene-based amplitude modulators. Furthermore, to improve the device performance, several schemes have been proposed recently [88–90]. Within a proposed structure shown in Figure 7C, Ye *et al.* [88] theoretically predicted that the 3-dB bandwidth of a graphene-based phase modulator can approach 119.5 GHz.

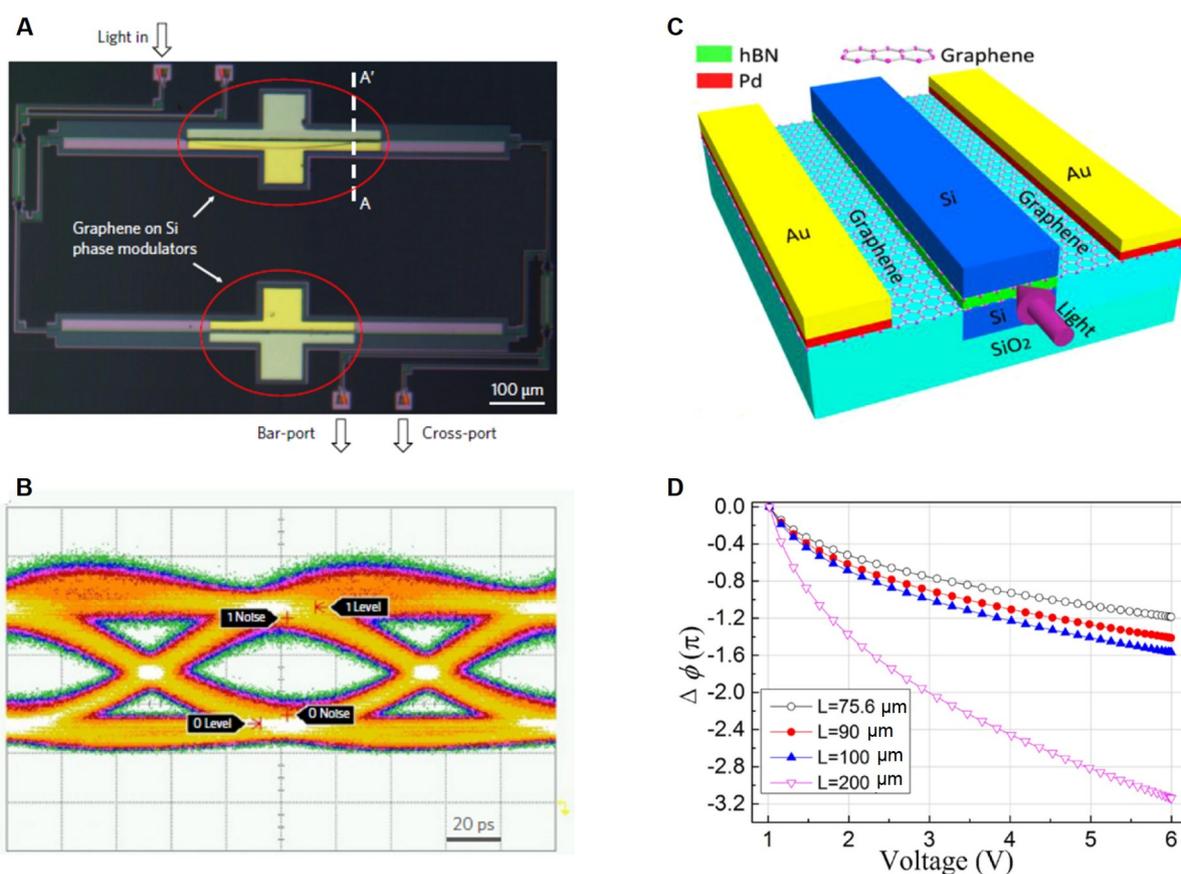


Figure 7 (A) Optical micrograph of a graphene phase modulator based on Mach-Zehnder interferometer. (B) Eye diagram at 10 Gb s⁻¹ [48]. (C) 3D view of an optical phase modulator based on a graphene-silicon hybrid waveguide. (D) Phase change curves under different applied voltages [88]. (A) and (B) Adapted with permission from [48]. Copyright 2018, Springer Nature. (C) and (D) Adapted with permission from [88]. Copyright 2017, IEEE.

Resonator-coupled optical modulators

To reduce the size of the device and increase the modulation depth, ring-coupled optical modulators are implemented. In 2014, Qiu *et al.* [35] demonstrated an optical modulator with gated graphene on a silicon micro-ring resonator. In that device, monolayer graphene is transferred onto the silicon-micro ring resonator. By tuning the Fermi level through electric gating, a ~40% amplitude modulation depth is obtained. In 2015, to enhance the modulation depth, Ding *et al.* [49] demonstrated a novel graphene-silicon micro-ring modulator, as shown in Figures 8A and 8B. In that device, a large power coupling coefficient is obtained by using a small coupling gap of ~150 nm. Meanwhile, the graphene coverage of ~25% on the ring waveguide is also promptly selected. Thus, the device initially works in slightly under-coupling condition and in critical coupling after tuning graphene. In this way, the modulation depth is enhanced to be 12.5 dB.

To further improve the speed of such micro-ring modulators, Phare *et al.* [50] demonstrated a high-speed micro-ring modulator based on graphene-Si₃N₄ hybrid waveguides. By using double-layer graphene, the graphene-graphene capacitor can be formed and the Fermi level of the graphene can be effectively changed. As shown in Figures 8C and 8D, a 22 Gb s⁻¹ non-return-to-zero eye diagram is measured while the 3-dB

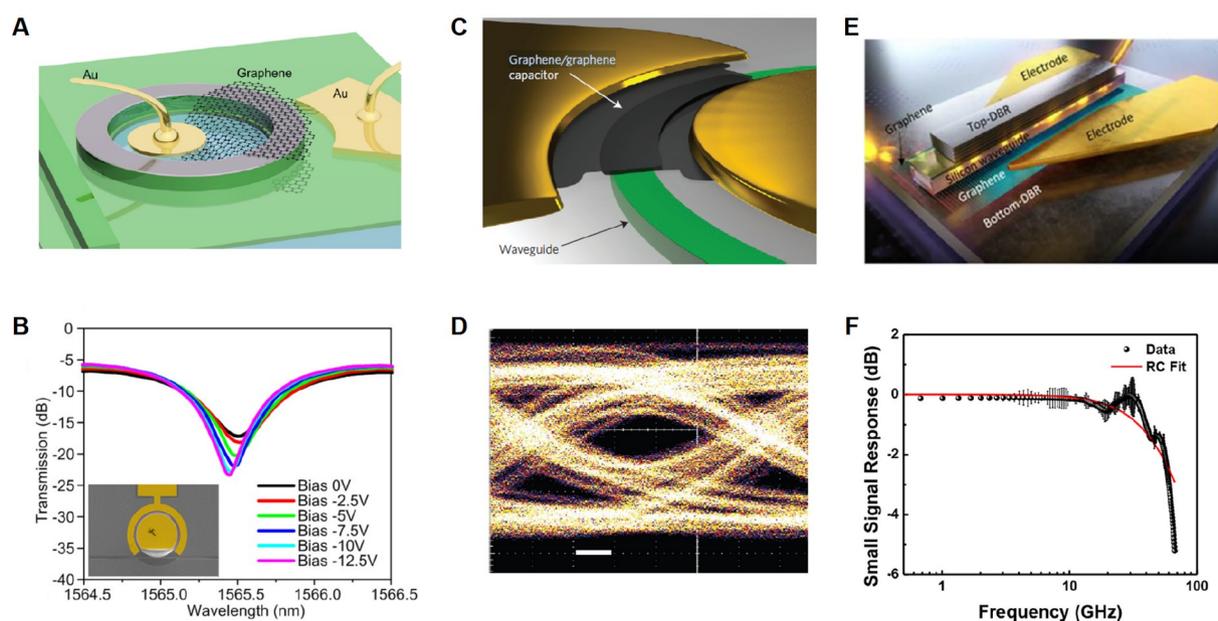


Figure 8 (A) Schematic diagram of a graphene-silicon microring modulator. (B) Measured transmission spectra at different voltages [49]. (C) Diagram of a graphene-Si₃N₄ micro-ring modulator. (D) Eye diagram at 22 Gb s⁻¹ [50]. (E) Schematic illustration of a graphene-based optical modulator using a Bragg reflector waveguide. (F) RF response of the double-layer graphene-based optical modulator [52]. (A) and (B) Adapted with permission from [49]. Copyright 2015, American Chemical Society. (C) and (D) Adapted with permission from [50]. Copyright 2015, Springer Nature. (E) and (F) Adapted with permission from [52]. Copyright 2022, De Gruyter.

bandwidth is 30 GHz. The power consumption is about 800 fJ bit⁻¹, which is limited by the relatively large capacitance of ~55 fF and the series resistor of ~100 Ω. Such an optical graphene-Si₃N₄ micro-ring modulator also shows its high performance under a low temperature of ~4.9 K [51].

To effectively decrease power consumption, one could reduce the electrical capacitance by shrinking the device size. In 2022, Heidari *et al.* [52] demonstrated an ultra-low power graphene-based optical modulator by using a Bragg reflector waveguide. In that device, a Bragg reflector waveguide is laterally merged with a silicon waveguide. Then, slow light is formed by using the distributed Bragg reflector (DBR) structure and the light-matter interaction is greatly enhanced within a 10 μm-long waveguide. Moreover, the capacitor can be decreased to 27 fF. In the demonstration, the measured 3-dB bandwidth is 60 GHz while the power consumption is only 2.25 fJ bit⁻¹. Such a modulator is advantageous for ultra-dense on-chip optical communication applications.

Performance comparison

To clearly show the performance of the chip-integrated graphene-based optical modulators, a partial list of typical chip-integrated graphene-based optical modulators is provided in Table 2. One can clearly find that resonator-based optical modulators would have high modulation depths, relatively low power consumptions and large bandwidths. Meanwhile, for waveguide-coupled optical modulators, electrical capacitances could be relatively large [47], which are limited by the device lengths. Then, the modulation bandwidths of waveguide-coupled optical modulators would be restrained. Moreover, based on demonstrated optical

Table 2 A partial list of typical chip-integrated graphene-based optical modulators

Structure and reference	Layers of graphene	Insertion loss (dB)	Modulation depth (dB)	Bandwidth (GHz)	Power consumption (fJ bit ⁻¹)
Non-planarized Si waveguide [40]	Monolayer	–	4	1.2	–
Planarized Si waveguide [41]	Monolayer	3.8	5.2	5.9	350
Planarized Si waveguide [34,42]	Monolayer	4	4.5	5	–
Non-planarized Si waveguide [43]	Double-layer	4	6.5	1	–
Non-planarized Si waveguide [44]	Double-layer	3.3	16	0.67	–
Planarized Si waveguide [45]	Double-layer	–	0.5	2.5	–
Non-planarized a-Si waveguide [46] ^{a)}	Double-layer	~1	2	35	1400
Planarized Si waveguide [47]	Double-layer	2.2	16	29	600
Planarized Si waveguide-MZI [48]	Monolayer	–	35	5	1000
Non-planarized Si waveguide-ring [35]	Monolayer	–	2.2	80 ^{b)}	–
Non-planarized Si waveguide-ring [49]	Monolayer	~5	12.5	–	–
Planarized Si ₃ N ₄ waveguide-ring [50]	Double-layer	–	15	30	800
Planarized Si ₃ N ₄ waveguide-ring [51]	Double-layer	–	7	14.7	–
Non-planarized a-Si waveguide-DBR [52] ^{a)}	Double-layer	–	~12	60	2.25

a) a-Si: amorphous silicon. b) Modulation bandwidth is obtained by calculation.

modulators, double-layer graphene-based optical modulators are preferred to obtain high-speed performances.

The device performances of the waveguide-coupled optical modulators might also be influenced by the fabrication process. For instance, after graphene transfer, broken area of graphene would exist and the initial fermi level of graphene cannot be controlled. Both these two factors could affect the loss from graphene. Thus, it could be difficult for resonator-coupled optical modulators to obtain high modulation depths and low insertion losses. Moreover, electrical capacitances would be relatively large for fabricated waveguide-coupled optical modulators, since their device lengths are usually longer than 100 μm . Such large capacitances would affect modulation speeds. Furthermore, compared with monolayer graphene EO modulators, double-layer graphene EO modulators can avoid the insertion loss from doped silicon. However, the mechanical transfer of the second graphene sheet is required for double-layer graphene modulators, which might increase the fabrication complexity and affect the device performances.

Free-space optical modulators

Free-space optical modulators can be used for high speed free-space optical communications, free-space optical computing, etc. [91]. Several silicon free-space optical modulators have been demonstrated, whose speeds, however, are relatively low (i.e., <150 MHz). By using the ultra-high carrier mobility of graphene, high-speed free-space modulations can be achieved by integrating graphene onto silicon optical devices. Moreover, in the mid-infrared range, a GHz spatial light modulator was demonstrated based on the graphene metasurface, in which a plasmonic resonance was formed [92]. Thus, GHz graphene-based near-infrared modulators could also be expected. Meanwhile, to obtain effective free space optical modulation, resonators should be introduced for near-infrared free-space optical modulators, since the optical absorption of monolayer graphene is only 2.3% for normal incident light.

To obtain free-space modulation, two free-space light modulators were demonstrated by Majumdar *et al.* [53] and Gan *et al.* [54], respectively. In these studies, normal incident light can be coupled into the photonic

crystal (PhC) cavity, based on a PhC cavity and a diffraction coupling scheme. Then, by tuning the Fermi level of graphene located on top of the PhC, the resonance wavelengths can be tuned by ~ 2 nm. High modulation depths of >6 dB can thus be achieved. However, in those two free-space modulators, the electrical capacitors are formed by ion-gel and thus the modulation speeds are limited.

To improve the modulation speed, Gao *et al.* firstly fabricated a high-performance modulator. As shown in Figure 9C, a BN/graphene/BN/graphene/BN five-layer stack is first prepared. Then, a silicon PhC membrane is transferred onto this stack [55]. In that device, the electrical capacitor is formed by a graphene-BN-graphene structure and thus a high speed can be achieved. The measured modulation depth is 3.2 dB and the bandwidth is ~ 1.2 GHz. Meanwhile, the capacitance of the device is 320 fF and the switching energy is approximately 1 pJ bit^{-1} . To improve the extinction ratio and the coupling efficiency, a high contrast grating (HCG) structure was utilized by Sun *et al.* [56]. In that device, graphene is transferred onto the HCG structure and the Fermi level of graphene can be tuned by using ion-gel. Then, a modulation depth of ~ 11 dB and a dynamic modulation speed of >1 MHz are obtained. Meanwhile, a graphene-based spatial light modulator was also obtained by using graphene/silicon nanohole array with a high modulation depth of >10 dB [57].

The performance parameters of the abovementioned free-space modulators are shown in Table 3. One can find that the modulation bandwidths of demonstrated devices would be lower than 1 MHz, if a top electrolyte gating with ion-gel is used [53,54,56,57]. However, their intrinsic bandwidths should be >1 GHz as the mid-infrared graphene-based modulator [92]. The bandwidths of those modulators can be increased by changing the gating schemes.

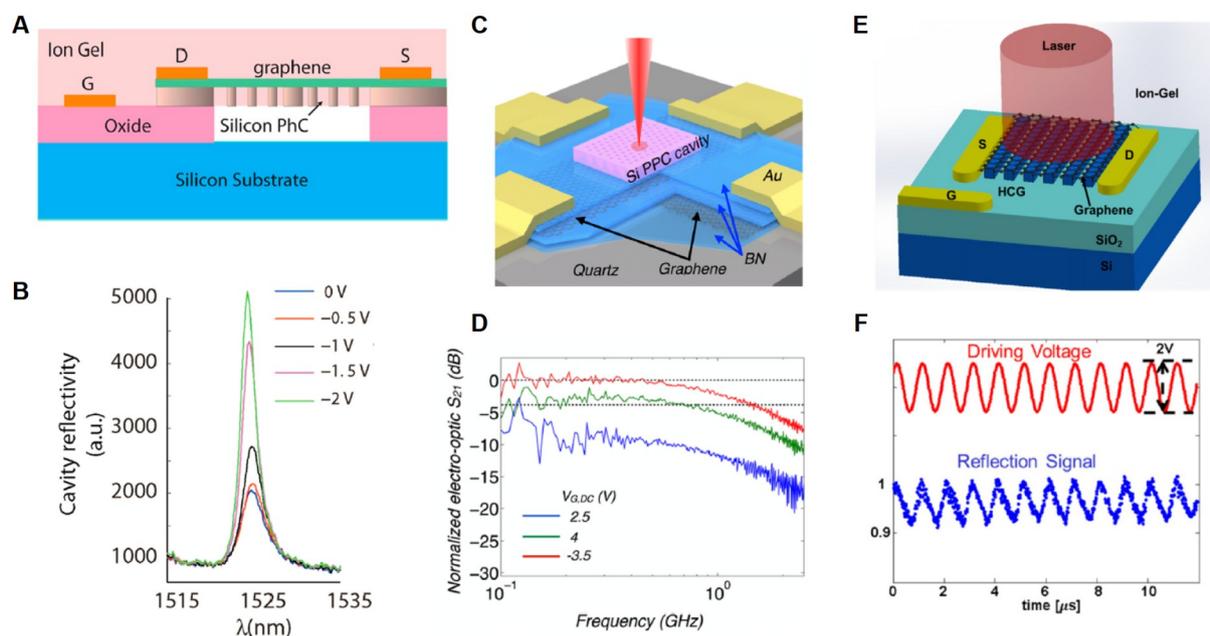


Figure 9 Graphene-based free-space optical modulators. (A) Schematic diagram of a graphene-PhC modulator. (B) Gate-voltage-induced differential reflection spectra at different voltages [53]. (C) Diagram of a cavity-graphene EO modulator. (D) Normalized EO S_{21} frequency responses at different DC gate voltages [55]. (E) Schematic of an HCG-graphene based spatial light modulator. (F) Reflection response at 1 MHz [56]. (A) and (B) Adapted with permission from [53]. Copyright 2013, American Chemical Society. (C) and (D) Adapted with permission from [55]. Copyright 2015, American Chemical Society. (E) and (F) Adapted with permission from [56]. Copyright 2016, Optica Publishing Group.

Table 3 A partial list of typical free-space graphene-based optical modulators

Structure and reference	Modulation depth (dB)	Bandwidth (MHz)	Power consumption (fJ bit ⁻¹)
Si photonic crystal cavity [53]	6	–	–
Si photonic crystal cavity [54]	>10	–	–
Si photonic crystal cavity [55]	3.2	1200	1000
Si high contrast grating [56]	11	1	–
Si nanohole array [57]	>10	0.001	–

Thermo-optic modulators

Thermo-optic modulators are quite useful in the data switching and tunable filters, in which low power is needed. For traditional integrated thermo-optic modulators, metal heaters are usually integrated. However, due to the strong optical absorption loss of metals, metal heaters cannot directly contact silicon waveguides. Thus, a buffer silica layer is usually introduced to reduce the propagation loss induced by metal layers. Then, in this way, the tuning efficiency would be decreased and the response time would be slow. Graphene has a much lower optical absorption compared with metals. Thus, graphene can directly contact with waveguides [93,94] and then increase the TO tuning efficiency. Meanwhile, the graphene has a large thermal conductivity, which can significantly improve the TO tuning speed.

Several graphene-based TO modulators have been demonstrated, as shown in Table 4. In 2015, Gan *et al.* [58] demonstrated a highly efficient TO micro-ring modulator assisted by graphene with a tuning efficiency of $\sim 0.104 \text{ nm mW}^{-1}$, as shown in Figures 10A and 10B. To further increase the tuning efficiency, one needs to shrink the size of the resonator and then reduce the optical mode volume. By using a race-track resonator with a bending radius of $2.5 \mu\text{m}$ and straight portions of $3 \mu\text{m}$, the mode volume can be effectively reduced. Then, a TO tuning efficiency of 0.24 nm mW^{-1} is obtained [59]. Meanwhile, a high-performance tunable silicon photonic microdisk resonator with a transparent graphene nanoheater was also demonstrated [60], as shown in Figures 10C and 10D. By using a micro-disk with a radius of $2 \mu\text{m}$, a high tuning efficiency of 1.67 nm mW^{-1} is achieved. Furthermore, a graphene-based TO modulator was also implemented based on a slow-light silicon photonic crystal waveguide, in which the light-matter interaction is enhanced, as shown in Figures 10E and 10F. In that device, a high tuning efficiency of 1.07 nm mW^{-1} is realized [38]. Moreover, based on a 1D PhC cavity with an ultrasmall mode volume, a tuning efficiency of 1.5 nm mW^{-1} was demonstrated. The response time for graphene-based TO devices is approximately $1 \mu\text{s}$, which is much faster than those of metal-heater based TO devices. That should be attributed to the high thermal conductivity of graphene.

Furthermore, as mentioned above, the TO efficiency can be effectively enhanced when the graphene layer

Table 4 Recent progress of graphene-silicon TO devices

Structure and reference	Tuning efficiency (nm mW ⁻¹)	Response time (μs)	Spacer
Graphene on Si micro-ring [58]	0.104	0.75	No
Graphene on Si racetrack-type resonator [59]	0.24	1.2	No
Graphene on Si micro-disk [60]	1.67	12.8	No
Graphene on Si PhC [38]	1.07	0.75	No
Graphene on Si nanobeam [39]	1.5	1.47	No
Graphene on Si micro-ring [37]	0.33	3	Yes

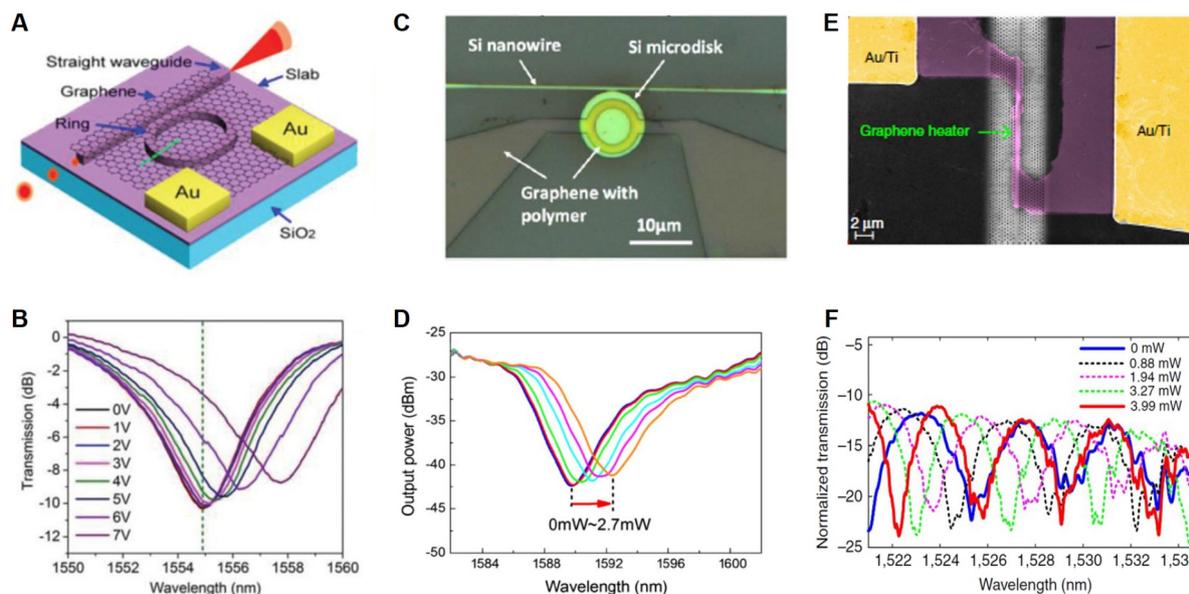


Figure 10 Graphene-based TO modulators. (A) 3D illustration of a graphene-based TO microring modulator. (B) Transmission spectra of the ring resonator at different drive voltages [58]. (C) Microscope image of a graphene-based TO microdisk modulator. (D) Spectral responses of the thermally tunable microdisk resonator as a function of the heat power [60]. (E) Colour scanning electron microscope image of a TO modulator based on slow-light-enhanced graphene micro-heater. (F) Static response of the heating power [38]. (A) and (B) Adapted with permission from [58]. Copyright 2015, Royal Society of Chemistry. (C) and (D) Adapted with permission from [60]. Copyright 2016, Optica Publishing Group. (E) and (F) Adapted with permission from [38]. Copyright 2017, Springer Nature.

directly contacts with the micro-resonators. However, the extinction ratio and quality factor of these resonators would also be degraded due to the loss from graphene. To solve this issue, a spacer based on hydrogen silsesquioxane (HSQ) and Al_2O_3 is introduced between the graphene layer and the silicon waveguide [37]. The height of the spacer is about 240 nm. Then, the loss induced by graphene can be reduced. In the experiment, the tuning efficiency is measured to be 0.33 nm mW^{-1} and the response time is $\sim 3 \mu\text{s}$.

Conclusion and perspective

In this review, we review the recent progress in the NIR graphene-based EO/TO optical modulators on silicon photonics platform. All these demonstrations show that graphene is a promising material to build high-speed and low-power optical modulators. In the future, to further reduce EO power consumption of chip-integrated optical modulators, graphene should be integrated onto ultra-compact resonators to reduce the electrical capacitances [95,96]. Meanwhile, to obtain ultra-fast speed modulators, series resistors should be decreased by optimizing the fabrication process. In this way, chip-integrated high-speed EO modulators with a modulation speed of $>100 \text{ GHz}$ [33,95] and a power consumption of $<1 \text{ fJ bit}^{-1}$ could be obtained [26]. Moreover, graphene is also an ideal material for the free-space light modulation. Inspired by the demonstration in the mid-infrared, high-speed near-infrared free space optical modulators could also be realized by using novel resonators and graphene. Such GHz free-space modulators could be very useful in spatial light communication, beam steering, optical communication, etc. For thermo-optic modulators, tuning efficiencies could be improved by enhancing the graphene-light interactions. In this way, a tuning efficiency of $>3 \text{ nm mW}^{-1}$

could be implemented [39]. In addition, future challenges for high-performance graphene-based optical modulators may lie in practical issues in mass production [65]. For on-chip integration of graphene-based optical modulators, it is still a big challenge to obtain large, uniform, and high-quality graphene. Moreover, to obtain high device performance uniformity, controllable and stable chemical doping for the graphene sheets is highly required. Furthermore, in practical application, there is still a significant need for better metal contacts and compatible insulators. All these challenges could in turn bring great opportunities and become the new driving force to improve the device performances in this field. In summary, the performance of graphene-based EO/TO modulators on silicon photonics platform would be further improved with rapid developments of technology and they would then play increasingly important roles in a wide range of applications, including but not limited to telecom, computing, quantum information processing, and beam steering.

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Conflict of interest

The authors declare no conflict of interest.

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